AMENDMENT TO THE SPECIFICATION

Amend the paragraph beginning at page 9, line 27 by rewriting the same as follows.

FIG. 4 is an alternative block diagram representation of portions of the system 10 shown in FIG. 1. As in the representation of FIG. 1, FIG. 4 shows a buffer 14 which temporarily stores incoming telephony signal data frames (incoming data stream not shown). Frame selection circuitry 80 is coupled to the buffer 14 to select telephony signal data frames for tone detection analysis. For example, the frame selection circuitry 80 may select every nth frame for tone detection analysis. The system 10 may also include frame analysis circuitry 82 which is coupled to and responsive to the frame selection circuitry 80. The frame analysis circuitry 82 may utilize, for example, FFT analysis or filter bank analysis to determine whether a signaling tone is present in each telephony signal data frame selected by the frame selection circuitry 80. The frame analysis circuitry 82 may also operate to extend the tone detection analysis forward and/or backward as required when a signaling tone is found to be present in a telephony signal data frame selected by the frame selection circuitry 80. The system 10 may further include subframe analysis circuitry 84 which is coupled to and responsive to the frame analysis circuitry 82. The ~~frame~~ subframe analysis circuitry 84 may operate to perform the "fine-tuning" analysis with respect to telephony signal data frames found by the frame analysis circuitry 82 to be beginning or ending frames of a signaling tone. This fine-tuning analysis may be performed with respect to subframes of the beginning and/or ending frames of the signaling tone, and may be as described above in connection with FIG. 3 or at 54 in FIG. 2.